

J1021 U.S. PTO
10/023147
12/18/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10023147	FILING DATE 12/18/2001	CLASS 235	SUBCLASS E15	GAU 2876	EXAMINER
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**APPLICANTS: Takano Kohji; Satoh Akashi;

**CONTINUING DATA VERIFIED: *EL*
NONE

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**FOREIGN APPLICATIONS VERIFIED: *EL*
JAPAN 2010-383039 12/19/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claim:

35 USC 119 condition met

Entered and Acknowledged Examiner's Initials

Arithmetic circuit to increase the speed for a modular multiplication for a public key system for encryption

yes ☒ no
yes ☐ no

ATTORNEY DOCKET NO

JP9-2000-0304-US1

U.S. DEPT. OF COMMERCE & TRADE SECRETARIAT

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Assistant Examiner

Total Claims

Print Claim for
O.G.

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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☐ CD-ROM

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